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AUG 08 2005

In re application of: Bangolae *et al*

Appl. No.: 09/924,722

Art Unit: 2663

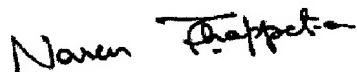
Filed: August 09, 2001

Examiner: Christine Ng

For: Reducing Overhead When Using
Loopback Cells for Fault Detection in
Bi-Directional Virtual Circuits

Atty. Docket: CSCO-009/4342

I hereby certify that this correspondence is being facsimile
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2005



(Signature)

By: Narendra R. Thappeta (Type Name)

**Amendment and Response to Final Office Action
Under 37 C.F.R. §§ 1.116**

Mail Stop **AF**
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action mailed June 15, 2005, Applicants submit the following remarks beginning at page number 2 of this paper.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those which may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to Deposit Account No.: 20-0674.